

FORM PTO-1449				Atty. Docket No. XA-9989		Appln. No. 10/715, 448	
LIST OF DOCUMENTS CITED BY APPLICANT				Applicant Hideyuki MATSUOKA et al.			
				Filing Date HEREWITH 11/19/03		Group 2818	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
<i>dh</i>	AA	2002-0093845	7/18/02	Matsuoka et al.	365	97	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
<i>dh</i>	AJ	2002-208682	7/26/02	Japan			Abstract
	AK						
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
<i>dh</i>	AP	Durlam et al., "A low power 1Mbit MRAM based on 1T1MTJ bit cell integrated with Copper Interconnects," 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 160-163.					
	AQ						
	AR						
Examiner <i>Neas</i>				Date Considered <i>04/13/05</i>			
XAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							